

**Docket No. 49704/DMC/A599  
Appln No. Not Assigned  
Amdt date August 19, 2004**

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1. (Original) A band pass filter formed on an integrated circuit (IC) chip, said band pass filter comprising:

a transformer capable of receiving an input signal and providing high pass filtering, said transformer comprising at least a pair of metallic spirals formed on the IC chip; and

a capacitor capable of receiving said input signal and providing low pass filtering in conjunction with an inductance of the transformer,

wherein said band pass filter provides band pass filtering through cascading said high pass and low pass filtering.

Claim 2. (Original) The band pass filter of claim 1, further comprising a plurality of band pass filter stages, each band pass filter stage comprising a capacitor and a transformer comprising a pair of metallic spirals formed on the IC chip, wherein said band pass filter stages are cascaded to form the band pass filter.

Claim 3. (Original) The band pass filter of claim 1, wherein said metallic spirals comprise copper spirals.

**Docket No. 49704/DMC/A599**

**Appln No. Not Assigned**

**Amdt date August 19, 2004**

Claim 4. (Original) The band pass filter of claim 1, wherein the transformer comprises a pair of transformers arrayed in series, wherein each transformer comprises a pair of metallic spirals.

Claim 5. (Original) The band pass filter of claim 1, wherein the metallic spirals are co-planer and have been interwound to form the transformer on the IC chip.

Claim 6. (Original) The band pass filter of claim 1, wherein the metallic spirals are stacked, one on top of the other, to form the transformer on the IC chip.

Claim 7. (Original) The band pass filter of claim 6, wherein the metallic spirals are separated by a dielectric material disposed therebetween.

Claim 8. (Original) The band pass filter of claim 7, wherein the dielectric material comprises silicon dioxide.

Claim 9. (Original) The band pass filter of claim 1, wherein communication circuitry is formed on the same IC chip as the transformed and the capacitor.

Claim 10. (Original) The band pass filter of claim 1, wherein a transformation ratio of the transformer is between approximately 1:1 and approximately 1:2.

**Docket No. 49704/DMC/A599**

**Appln No. Not Assigned**

**Amdt date August 19, 2004**

Claim 11. (Original) The band pass filter of claim 1, wherein the IC chip comprises a silicon substrate.

Claim 12. (Original) The band pass filter of claim 1, wherein the metallic spirals have a substantially rectangular or square overall shape.

13. (Original) A communication system-on-chip (SOC) comprising communication circuitry and a band pass filter formed on an integrated circuit (IC) chip, said band pass filter comprising:

a transformer capable of receiving an input signal and providing high pass filtering, said transformer comprising at least a pair of metallic spirals formed on the IC chip; and

a capacitor capable of receiving said input signal and providing low pass filtering in conjunction with an inductance of the transformer,

wherein said band pass filter provides band pass filtering through cascading said high pass and low pass filtering.

14. (Original) The communication SOC of claim 13, wherein the band pass filter further comprises a plurality of band pass filter stages, each band pass filter stage comprising a capacitor and a transformer comprising a pair of metallic spirals formed on the IC chip, wherein said band pass filter stages are cascaded to form the band pass filter.

**Docket No. 49704/DMC/A599**

**Appln No. Not Assigned**

**Amdt date August 19, 2004**

15. (Original) The communication SOC of claim 13, wherein said metallic spirals comprise copper spirals.

16. (Original) The communication SOC of claim 13, wherein the transformer comprises a pair of transformers arrayed in series, wherein each transformer comprises a pair of metallic spirals.

17. (Original) The communication SOC of claim 13, wherein the metallic spirals are co-planer and have been inter-wound to form the transformer on the IC chip.

18. (Original) The communication SOC of claim 13, wherein the metallic spirals are stacked, one on top of the other, to form the transformer on the IC chip.

19. (Original) The communication SOC of claim 18, wherein the metallic spirals are separated by a dielectric material disposed therebetween.

20. (Original) The communication SOC of claim 19, wherein the dielectric material comprises silicon dioxide.

21. (Original) The communication SOC of claim 13, wherein a transformation ratio of the transformer is between approximately 1:1 and approximately 1:2.

22. (Original) A communication device comprising:

**Docket No. 49704/DMC/A599**

**Appln No. Not Assigned**

**Amdt date August 19, 2004**

a communication system-on-chip (SOC) comprising communication circuitry and a band pass filter formed on an integrated circuit (IC) chip, said band pass filter comprising:

a transformer capable of receiving an input signal and providing high pass filtering, said transformer comprising at least a pair of metallic spirals formed on the IC chip; and

a capacitor capable of receiving said input signal and providing low pass filtering in conjunction with an inductance of the transformer,

wherein said band pass filter provides band pass filtering through cascading said high pass and low pass filtering.

23. (Original) The communication device of claim 22, wherein the band pass filter further comprises a plurality of band pass filter stages, each band pass filter stage comprising a capacitor and a transformer comprising a pair of metallic spirals formed on the IC chip, wherein said band pass filter stages are cascaded to form the band pass filter.

24. (Original) The communication device of claim 22, wherein said metallic spirals comprise copper spirals.

25. (Original) The communication device of claim 22, wherein the transformer comprises a pair of transformers arrayed in series, wherein each transformer comprises a pair of metallic spirals.

**Docket No. 49704/DMC/A599**

**Appln No. Not Assigned**

**Amdt date August 19, 2004**

26. (Original) The communication device of claim 22, wherein the metallic spirals are co-planer and have been interwound to form the transformer on the IC chip.

27. (Original) The communication device of claim 22, wherein the metallic spirals are stacked, one on top of the other, to form the transformer on the IC chip.

28. (Original) The communication device of claim 27, wherein the metallic spirals are separated by a dielectric material disposed therebetween.

29. (Original) The communication device of claim 28, wherein the dielectric material comprises silicon dioxide.

30. (Original) The communication device of claim 22, wherein a transformation ratio of the transformer is between approximately 1:1 and approximately 1:2.